

FIG. 1

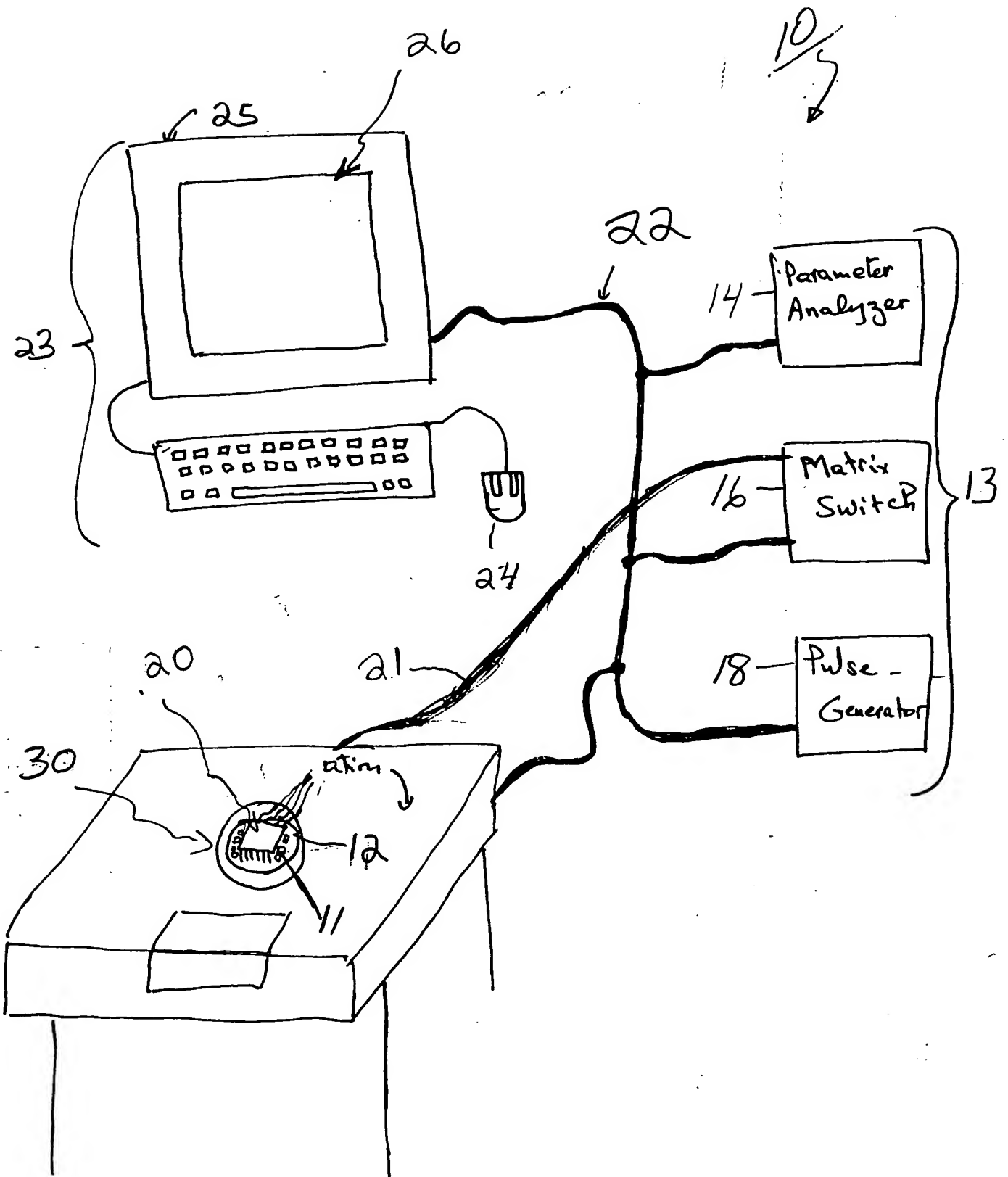


Fig 1

10032876 1003401

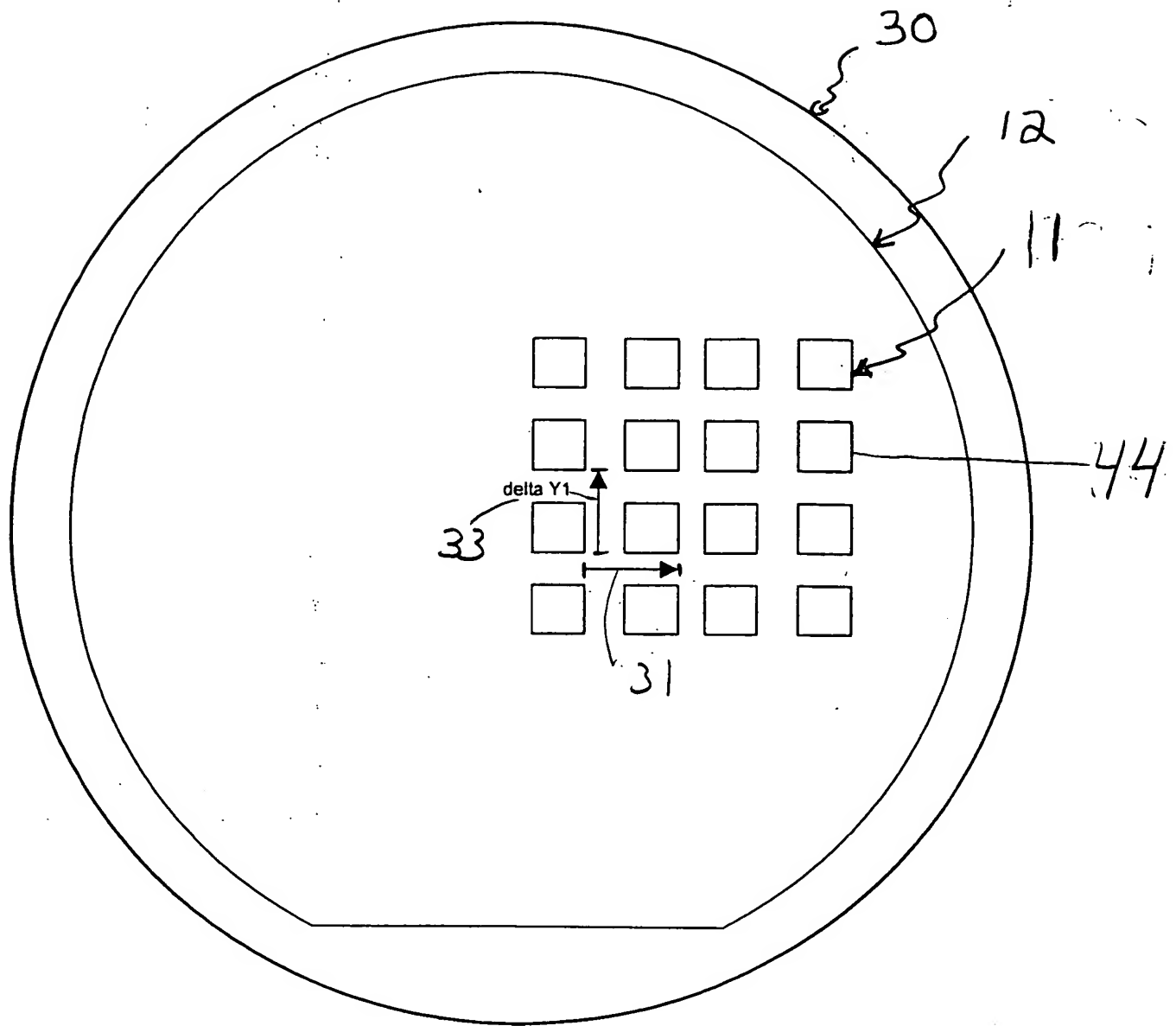


Fig 2

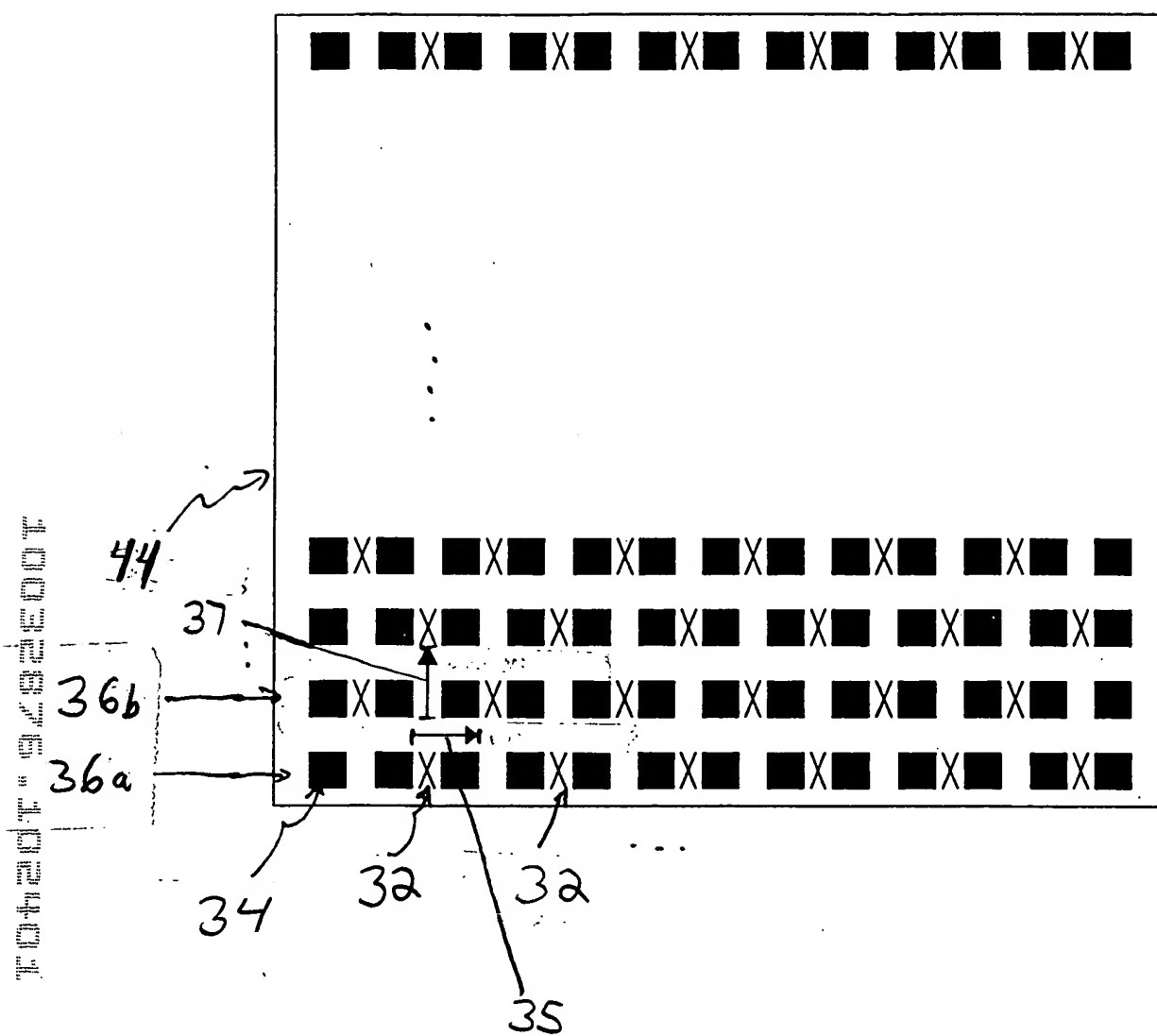


Fig 3

10032375-102401

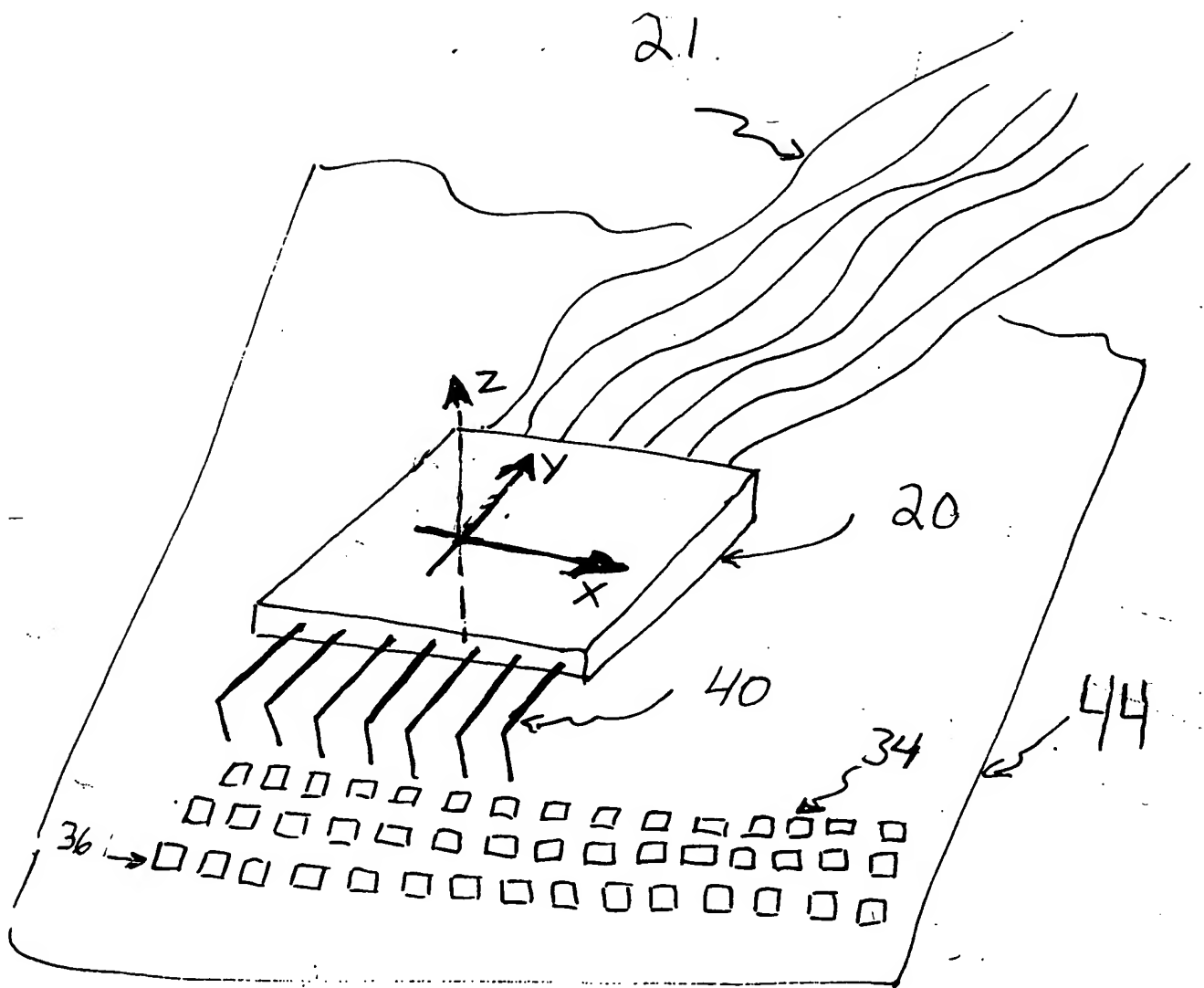
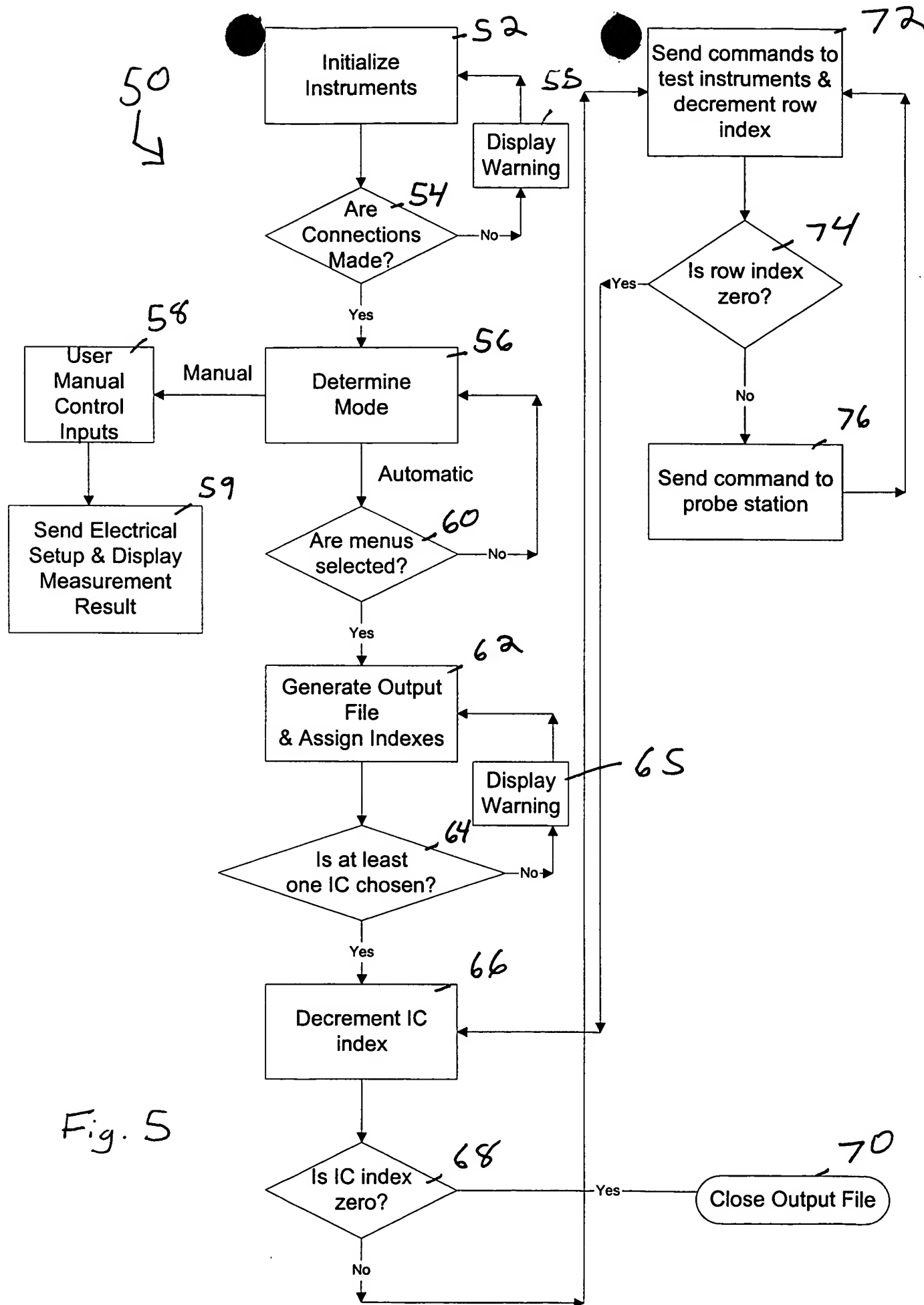


Fig 4



10367-10401

The screenshot shows the Borland Visual J++ development environment. The main window displays a Java Swing application titled "Die Cell Varsity". The window has a title bar with standard OS controls and a menu bar with "File", "Product", "Mapvision", and "Help". The main content area contains a single button labeled "Go Home?". Below the button, the text "Die Varsity Library" is visible. The status bar at the bottom indicates "Die Cell Varsity Library: OK" and "10".

In the background, the IDE's interface is visible, including the "Product" window showing "Product: Solid curves for Borland 1 (Blue: V_BOR, Red: V_OWN, Green: LBOR)" and the "Mapvision" window showing the project structure.

Manual Control

Stop

Load

Out

Matrix Switch (E5250A)

Card 1 (E5252A)

1 2 3 4 5 6 7 8 9 10 11 12

13 14 15 16 17 18 19 20 21 22 23 24

25 26 27 28 29 30 31 32 33 34 35 36

Card 2 (E5252A)

1 2 3 4 5 6 7 8 9 10 11 12

13 14 15 16 17 18 19 20 21 22 23 24

25 26 27 28 29 30 31 32 33 34 35 36

Channel A

period: 100

pulse width: 30

delay: 10

low volt: high volt

0 21

ON OFF

Channel B

period: 100

pulse width: 50

delay: 0

low volt: high volt

0 21

ON OFF

Both Channels

ON OFF

Matrix Switch

Correct Disconnect

Fig. 7

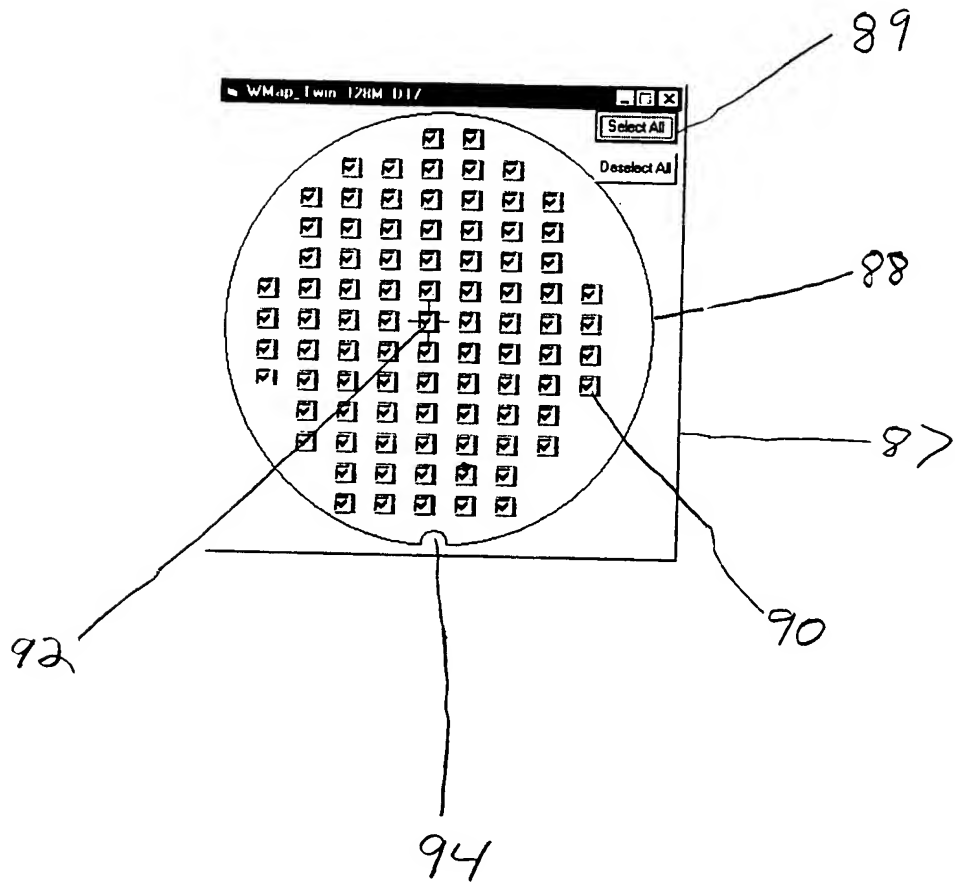


FIG. 8.

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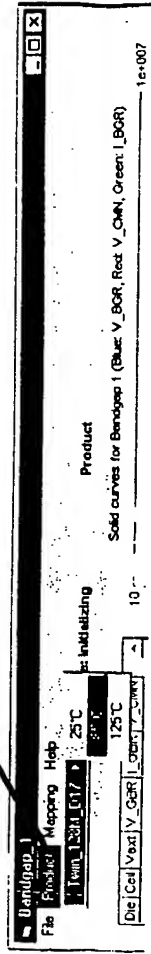


FIG- 9A

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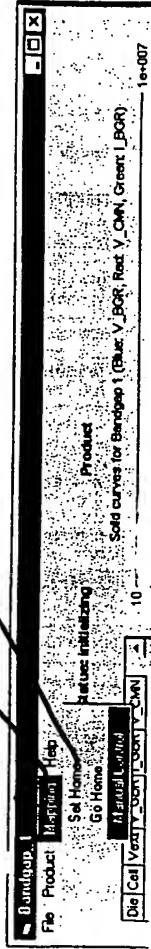


FIG- 9B

77 74 79

80

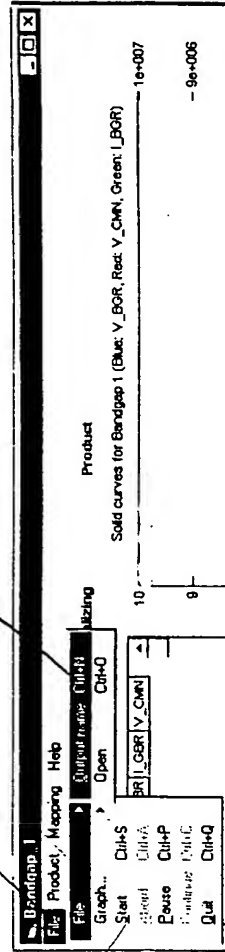


Fig 10

Product Twin_128M_D17 @ Temp. 25 °C

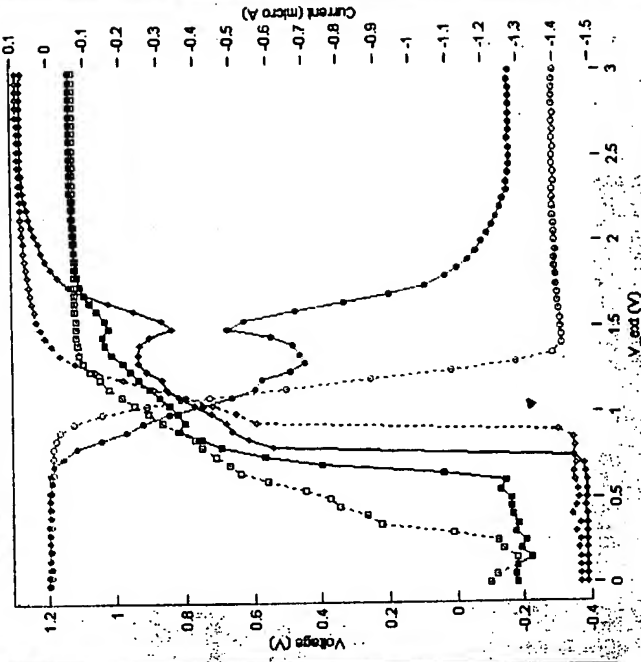
Bandgap_1

File Product Mapping Help

Status: measuring

Product Twin_128M_D17 @ Temp. 25 °C

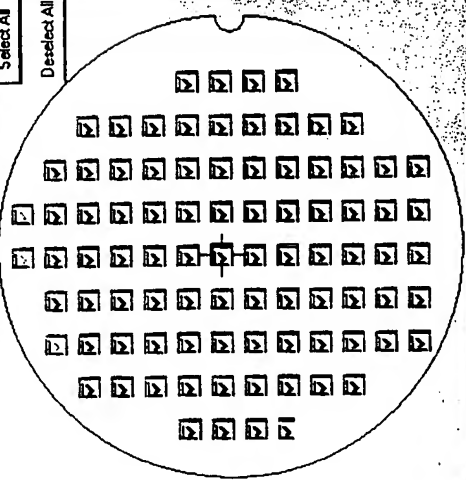
Solid curves for Bandgap 1 (Blue: V_BOR, Red: V_CIN, Green: L_BOR)



Die	Cell	V_BOR	V_CIN	V_CIN
3	2	0	-0.368	17.5E-10
3	2	0.05	-0.368	18.1E-10
3	2	0.1	-0.368	16.9E-10
3	2	0.15	-0.368	17.6E-10
3	2	0.2	-0.364	16.7E-10
3	2	0.25	-0.367	83.1E-11
3	2	0.3	-0.357	16.6E-10
3	2	0.35	-0.368	50.5E-11
3	2	0.4	-0.345	20.2E-10
3	2	0.45	-0.356	22.3E-10
3	2	0.5	-0.364	61.2E-11
3	2	0.55	-0.35	43.3E-11
3	2	0.6	-0.352	56.2E-10
3	2	0.65	-0.347	10.6E-8
3	2	0.7	-0.356	94.9E-10
3	2	0.75	-0.348	81.6E-10
3	2	0.8	-0.352	91.1E-10
3	2	0.85	-0.347	15.8E-8
3	2	0.9	-0.301	30.5E-8
3	2	0.95	-0.592	57.2E-8
3	2	1	0.652	14.7E-8
3	2	1.05	0.719	26.9E-8
3	2	1.1	0.799	44.1E-8
3	2	1.15	0.888	65.3E-8
3	2	1.2	0.979	88.4E-8

Select All

Deselect All



Measurement, light 0.4V bump
& Automation

1:51 PM

Bandgap_1

Project2 - Microsd...

Exploring - C:\OS...

WinTV32

Nucleus

default.cfg - Syste...

Fig. 11

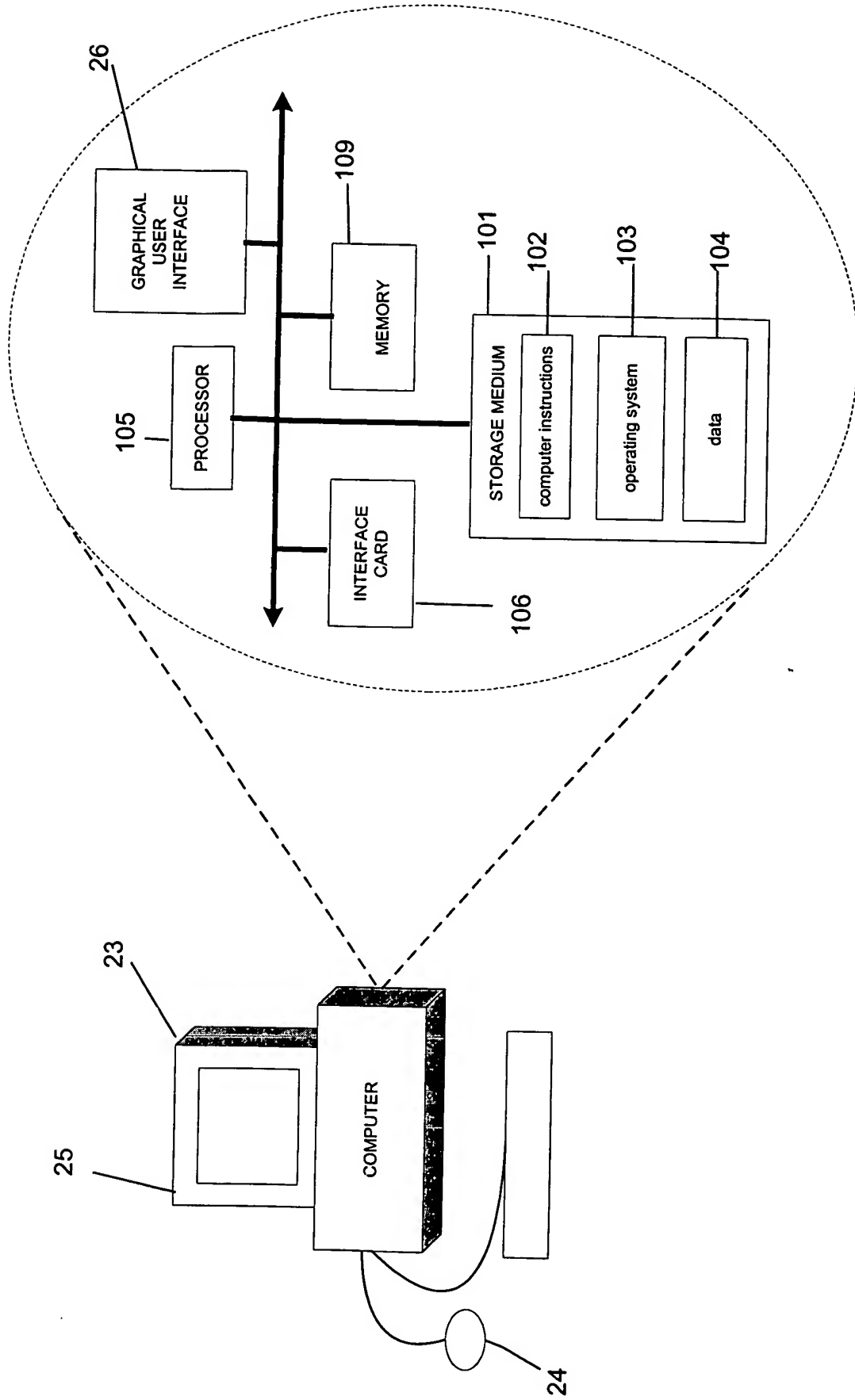


FIG. 12